



10/15/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER:

U.S. **UTILITY** Patent Application

 O.I.P.E. SCANNED <u>748 4</u> Q.A. 	PATENT DATE
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APPLICATION NO. 09/976341	CONT/PRIOR F	CLASS 257	SUBCLASS 5	ART UNIT 2015	EXAMINER J. J. J.
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## APPLICANTS

Takashi Terauchi  
Akinobu Teramoto

**TITLE**

Method of manufacturing semiconductor device, and semiconductor device having memory cell

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____     	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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